

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	660	transistors and dcc	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/24 16:27
L2	36	dcc with first with second with clock	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/24 16:28
L3	0	2 and control adj circuit with detect\$3 and affect\$3 and characteristic with transistor\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/24 16:30
L4	230	1 and clock adj signals	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/24 16:30
L5	1	4 and control adj circuit with detect\$3 and variation with transistors	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/24 16:35
L6	441	1 and characteristic	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/24 16:50
L7	2501500	6and variation	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/24 16:50
L8	248	6 and variation	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/24 16:51

## EAST Search History

L9	30796	7 and characteristic with transistor\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/24 16:52
L10	31	8 and characteristic with transistor\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/24 16:52
L11	9	10 and slew adj rates	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/24 16:53
L12	0	11 and adjust\$3 adj capacit\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/24 16:53
L13	9	11 and adjust\$3 and capacit\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/24 16:59
L14	9	13 and clock adj signals	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/24 17:04
L15	3684	365/233.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/24 17:04
L16	0	15 and dcc and first adj clock and second adj clock and characteristic with trnasistors and slew adj rates	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/24 17:05

## EAST Search History

L17	0	15 and dcc and first adj clock and second adj clock and characteristic with transistors and slew adj rates	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/24 17:05
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